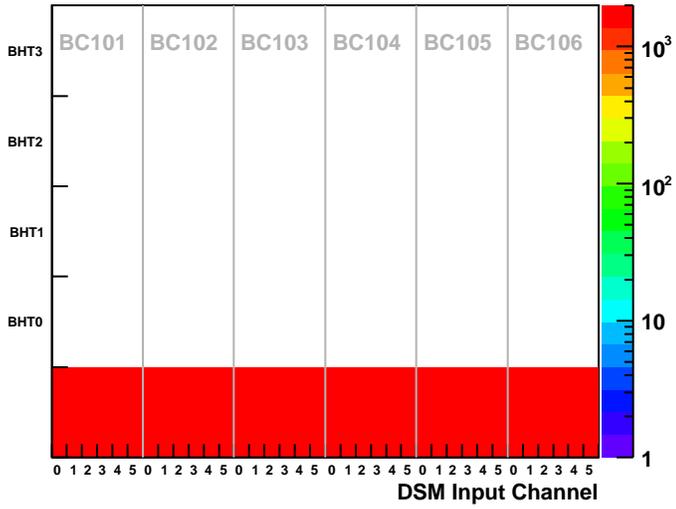


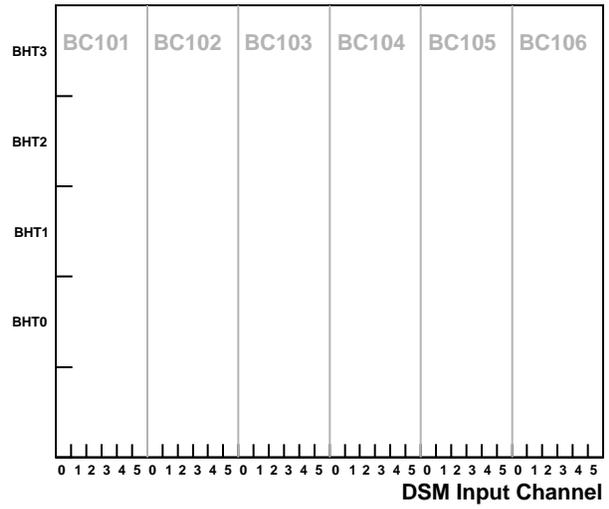
Barrel EMC L1 Input - High Tower Bits

Entries 72000



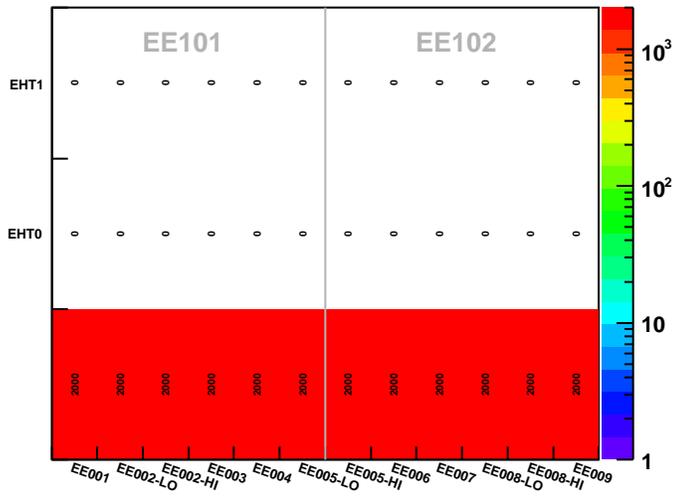
Barrel EMC L1 Input - High Tower Bits Errors

Entries 0



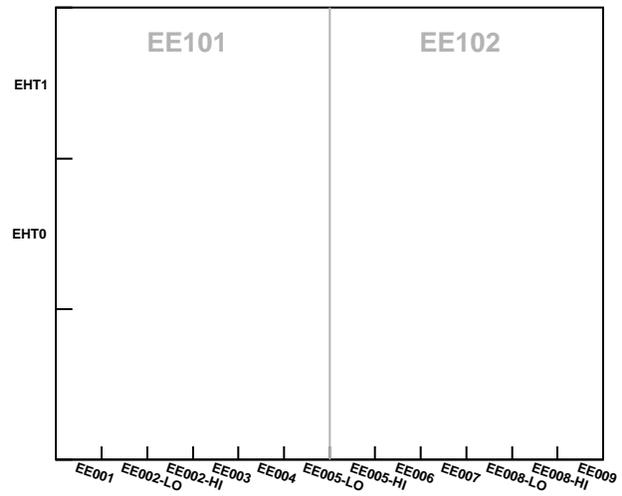
Endcap EMC L1 Input - High Tower Bits

Entries 24000



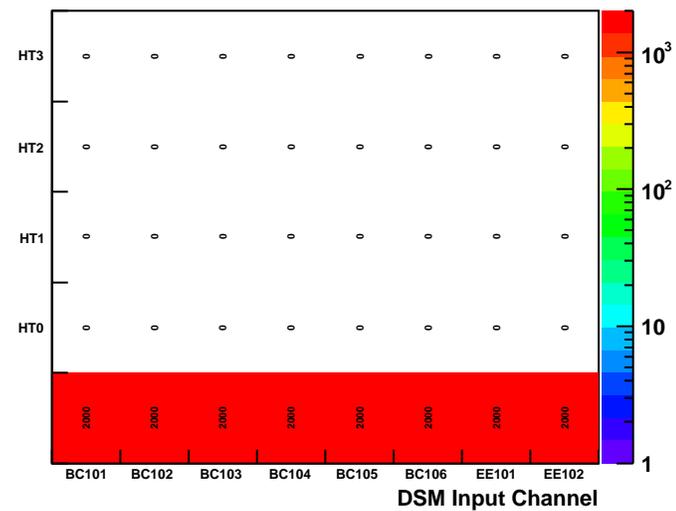
Endcap EMC L1 Input - High Tower Bits Errors

Entries 0



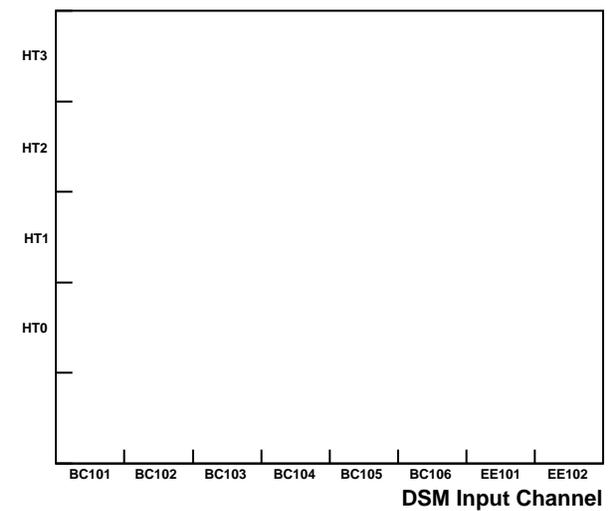
EMC L2 Input - High Tower Bits

Entries 16000



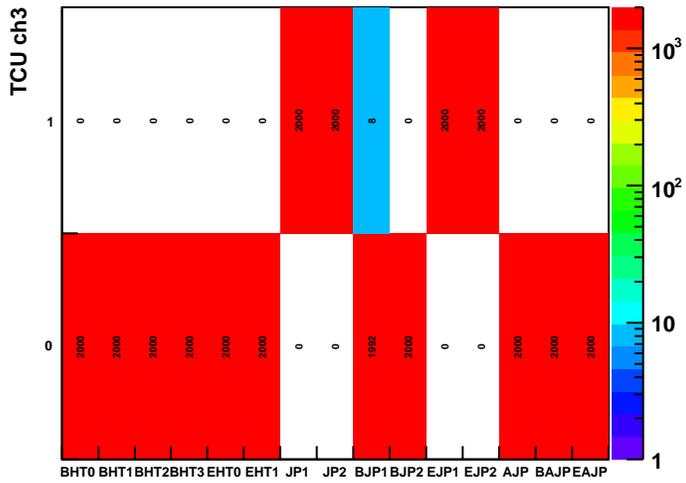
EMC L2 Input - High Tower Bits Errors

Entries 0



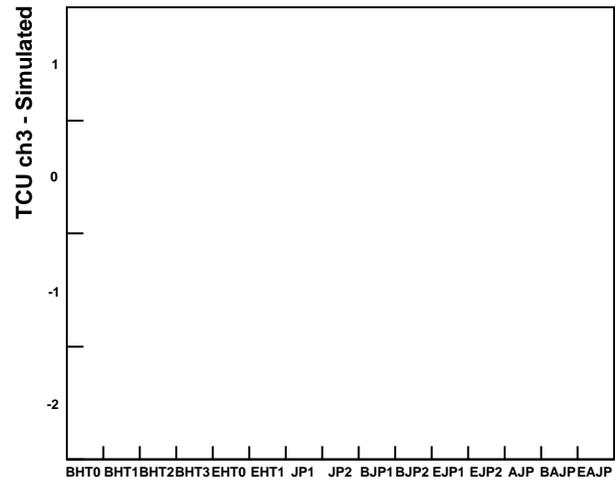
TCU ch3 Input (EM201)

Entries 30000



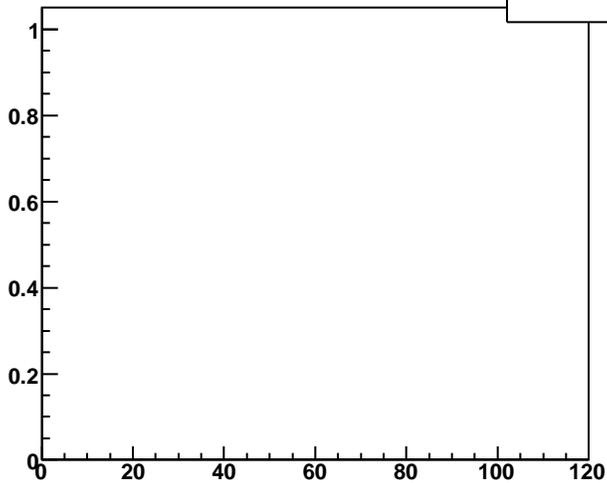
TCU ch3 Input (EM201)

Entries 0



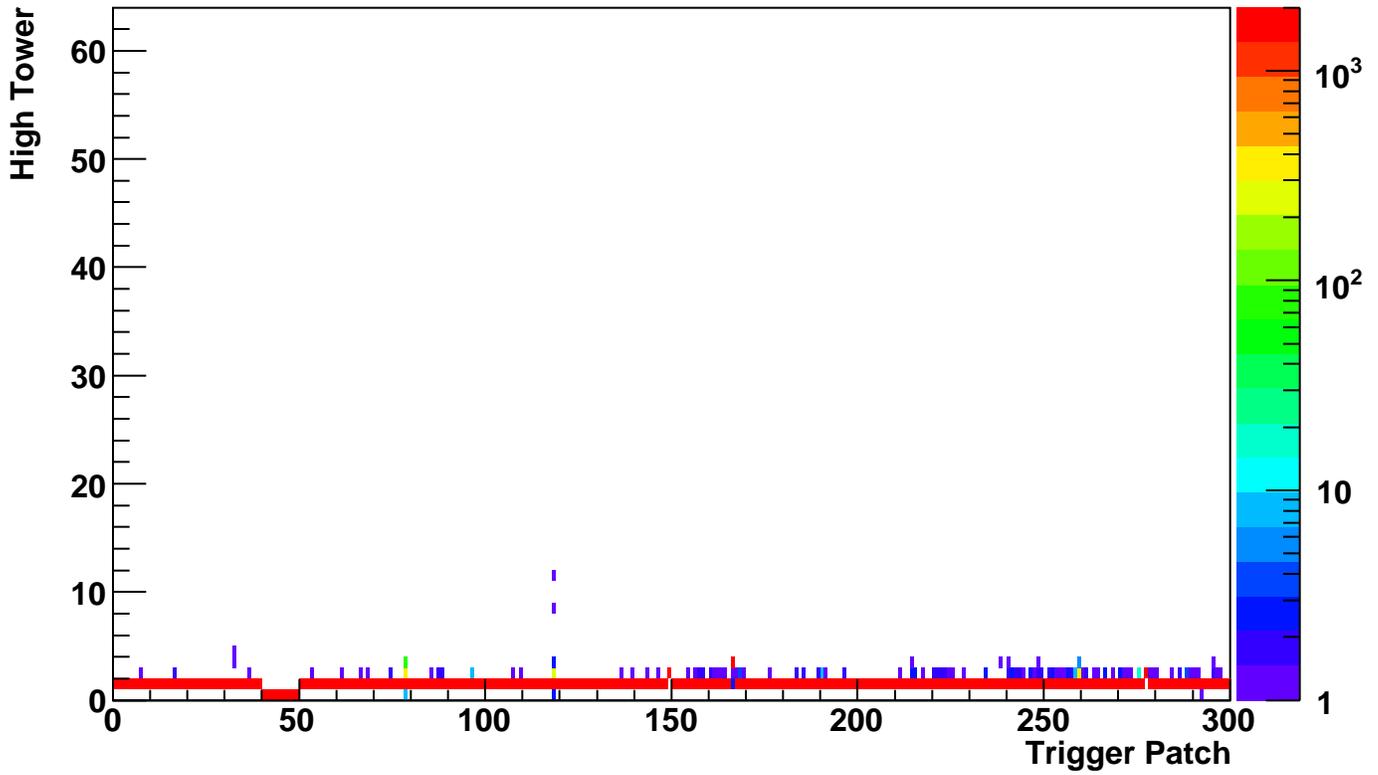
BunchId7Bit (BHT3)

Entries 0



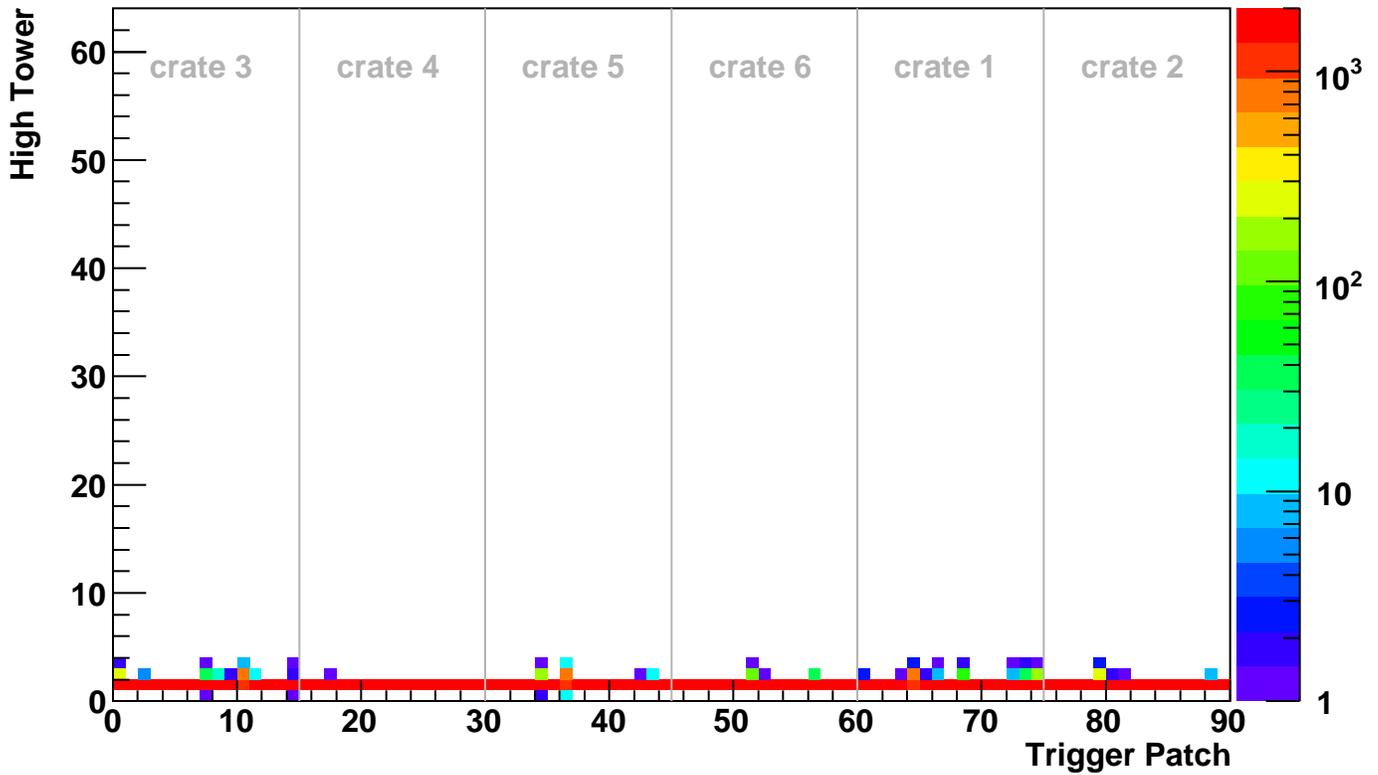
Barrel EMC L0 Input - High Tower

Entries 600000



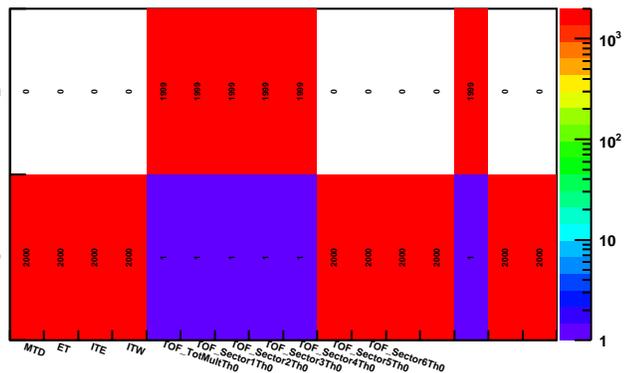
Endcap EMC L0 Input - High Tower

Entries 180000



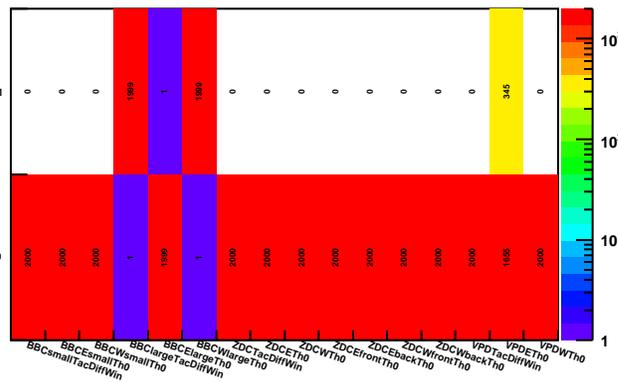
TF201 0-15 (ch0)

Entries 32000



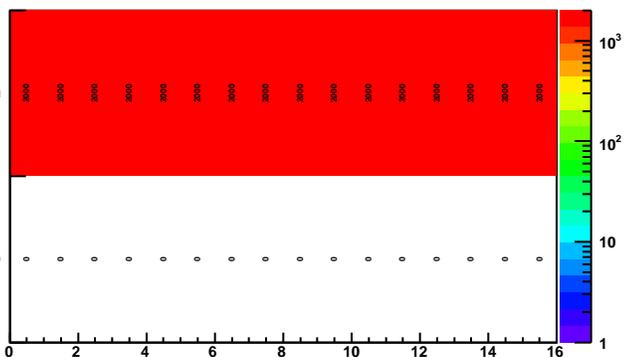
VT201 0-15 (ch1)

Entries 32000



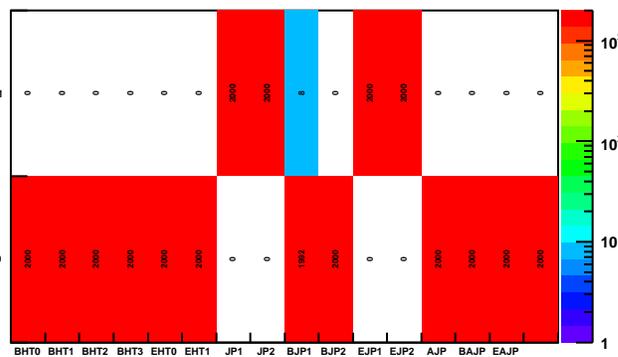
Unused (ch2)

Entries 32000



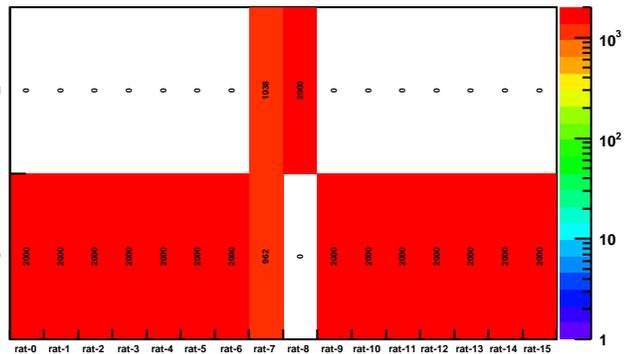
EM201 0-15 (ch3)

Entries 32000



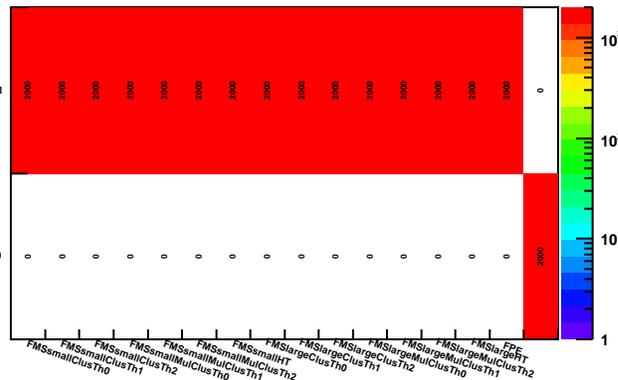
RAT board (ch4)

Entries 32000



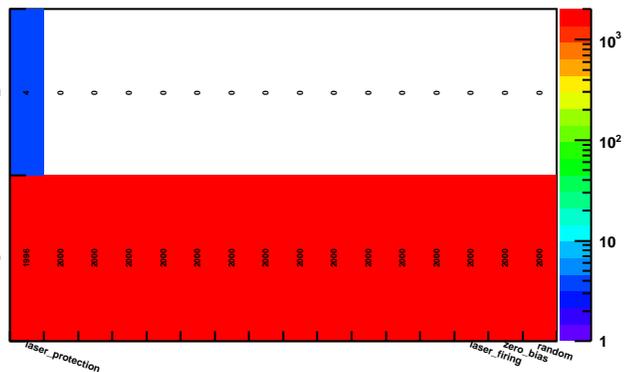
FP201 0-15 (ch5)

Entries 32000



ST201 0-15 (ch6)

Entries 32000



Unused (ch7)

Entries 32000

